



Workshop
IHP – FEEIT joint research initiative

Faculty of Electrical Engineering and IT - Skopje



Workshop agenda – Day 1 (10. Nov. 2015) - FEEIT Conference Hall

09.30 – 11.30 Introduction of the joint research groups

1. **Introduction of KTI research group and the FEEIT study programs** (30 min. + 10 min.) - Marija Kalendar
2. **Introduction of IHP** (30 min. + 10 min.) - Rolf Kraemer
3. **Overview of the Design for Testability (DFT) group in IHP** (30 min. + 10 min.) - Milos Krstic

LUNCH BREAK

12.30 – 17.30 Dependable (multi)processor systems - Themes of possible cooperation

4. **Design of novel memory-centric architecture and organization of processors** (30 min. + 10 min.) - Danijela Efnusheva
5. **Dependable systems** (45 min. + 15 min.) - Rolf Kraemer
6. **Architectural framework for dependable multiprocessors** (40 min. + 10 min.) - Aleksandar Simevski

BREAK

7. **Compiler and simulator design for a registerless computer architecture** (25 min. + 5 min.) - Goce Dokoski
8. **A cryptoprocessor based on the public-private key encryption mechanism** (20 min. + 10 min.) - Goce Dokoski

Workshop agenda – Day 2 (11. Nov. 2015) - FEEIT Conference Hall

09.00 – 12.00 High performance wireless systems

1. **Baseband VHDL Design for a 60GHz wireless system** (45 min. + 15 min.)

Marija Kalendar

2. **Hi-performance wireless systems** (45 min. + 15 min.)

Rolf Kraemer

3. **RFID/NFC-based security solutions for healthcare cloud system**

and **Implementing a new Linux GA scheduler** – 2 presentations (40 min + 10 min.)

Goran Jakimovski

LUNCH BREAK

13.00 – 17.00 System design

4. **Integrated circuit design flow** (45 min. + 15 min.) - Aleksandar Simevski

5. **Low-power trade-offs in fault-tolerant ASICs** (45 min. + 15 min.) - Milos Krstic

BREAK

6. **Channel coders – VHDL design** (30 min. + 10 min.) - Maja Malenko

7. **Hi-tech corp. presentation** (tentative)